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IFW 2183 JS



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Leber et al.

Examiner: Kevin P. Rizzuto

Serial No: 09/683,351

Group Art Unit: 2183

Filed: February 18, 2001

Docket: DE920000097US1 (8728-729)

For: METHOD FOR HANDLING 32 BIT RESULTS FOR AN OUT-OF-
ORDER PROCESSOR WITH A 64 BIT ARCHITECTURE

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 C.F.R. § 1.111

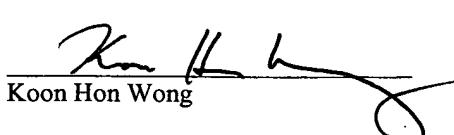
Sir:

This is a response to the Office Action mailed November 8, 2004. Please amend
the above-identified application as follows:

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class
mail, postpaid in an envelope, addressed to: Mail Stop Amendment, Commissioner for Patents, PO Box
1450, Alexandria VA, 22313-1450 on March 8, 2005.

Dated: 3/8/05


Koon Hon Wong